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ABSTRACT

An in-plane switching mode liquid crystal display device includes first and second substrates and a plurality of gate and data bus lines defining pixel regions and arranged perpendicularly and/or horizontally on the first substrate. A common line is formed with the gate bus line, and a plurality of thin film transistors are formed at respective crossing areas of the gate and data bus lines. A plurality of gate electrodes are connected to the gate bus lines, and a gate insulator having a contact hole on the gate electrodes. A transparent first metal layer including a plurality of first electrodes is on the gate insulator. A passivation layer having a contact hole is on the first metal layer. A transparent second metal layer including a plurality of second electrodes is on the passivation layer. A black matrix for preventing light from leaking around the thin film transistor, the gate bus line, and data bus line is on the second substrate. A color filter layer is on the second substrate, and a liquid crystal layer is between the first and second substrates.